L Number	Hits	Search Text	DB	Time stamp
1	1079	700/121.ccls.	USPAT;	2004/10/13 22:43
			US-PGPUB;	
			EPO; JPO;	
		/	IBM_TDB	2004/10/12 22 51
2	218	700/121.ccls. and (delay or hysteresis)	USPAT; US-PGPUB;	2004/10/13 22:51
			EPO; JPO;	
			IBM TDB	
3	210	700/121.ccls. and (delay or hysterisis)	USPAT;	2004/10/13 22:44
		( a a a a a a a a a a a a a a a a a a a	US-PGPUB;	, ,
			EPO; JPO;	
			IBM_TDB	
4	218	(700/121.ccls. and (delay or hysterisis))	USPAT;	2004/10/13 22:44
		or (700/121.ccls. and (delay or hysteresis)	US-PGPUB; EPO; JPO;	
		,	IBM TDB	
5	176	((700/121.ccls. and (delay or hysterisis) )	USPAT;	2004/10/13 22:57
		or (700/121.ccls. and (delay or hysteresis)	US-PGPUB;	
		)) and wafer	EPO; JPO;	
			IBM_TDB	
6	111	(((700/121.ccls. and (delay or hysterisis))	USPAT;	2004/10/13 22:57
		or (700/121.ccls. and (delay or hysteresis)	US-PGPUB;	
		)) and wafer) and (model\$5 or simulat\$5 or emulat\$5)	EPO; JPO; IBM TDB	
7	27	((((700/121.ccls. and (delay or hysterisis)	USPAT;	2004/10/13 22:52
'		) or (700/121.ccls. and (delay or mysterists)	US-PGPUB;	
		hysteresis) )) and wafer) and (model\$5 or	EPO; JPO;	To the state of th
		simulat\$5 or emulat\$5)) and (voltage and	IBM_TDB	
		current)		
8	4	1,1,1,1,1,1,1,1,1,1,1,1,1,1,1,1,1,1,1,	USPAT;	2004/10/13 22:57
		) or (700/121.ccls. and (delay or	US-PGPUB;	
		hysteresis) )) and wafer) and (model\$5 or simulat\$5 or emulat\$5)) and (threshold with	EPO; JPO; IBM TDB	
		voltage)	TBM_TDB	
9	95	700/108.ccls. and (delay or hysteresis)	USPAT;	2004/10/13 22:57
		• •	US-PGPUB;	, ,
		·	EPO; JPO;	
			IBM_TDB	(
10	93	700/108.ccls. and (delay or hysterisis)	USPAT;	2004/10/13 22:52
			US-PGPUB; EPO; JPO;	
			IBM TDB	
11	95	(700/108.ccls. and (delay or hysteresis) )	USPAT;	2004/10/13 22:52
		or (700/108.ccls. and (delay or hysterisis)	US-PGPUB;	
		])	EPO; JPO;	
			IBM_TDB	
12	34	((700/108.ccls. and (delay or hysteresis))	USPAT;	2004/10/13 22:52
		or (700/108.ccls. and (delay or hysterisis) )) and wafer	US-PGPUB; EPO; JPO;	-
		,, and water	IBM TDB	
13	24	(((700/108.ccls. and (delay or hysteresis))	USPAT;	2004/10/13 22:52
		or (700/108.ccls. and (delay or hysterisis)	US-PGPUB;	
		)) and wafer) and (model\$5 or simulat\$5 or	EPO; JPO;	
		emulat\$5)	IBM_TDB	0004/55/55 == :
14	7	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2004/10/13 22:52
		) or (700/108.ccls. and (delay or hysterisis) )) and wafer) and (model\$5 or	US-PGPUB; EPO; JPO;	
		simulat\$5 or emulat\$5)) and (woltage and	IBM TDB	
		current)	12371_123	
15	2	1	USPAT;	2004/10/13 22:52
		) or (700/108.ccls. and (delay or	US-PGPUB;	
		hysterisis) )) and wafer) and (model\$5 or	EPO; JPO;	
		simulat\$5 or emulat\$5)) and (threshold with	IBM_TDB	
16	50	voltage) 700/109.ccls. and (delay or hysterisis or	HCDATT.	2004/10/13 22:58
10		hysteresis)	USPAT; US-PGPUB;	2004/10/13 22:58
			EPO; JPO;	
			IBM_TDB	

Page 1

17		18	(700/109.ccls. and (delay or hysterisis or hysteresis) ) and wafer	USPAT; US-PGPUB;	2004/10/13 22:58
			hysteresis, , and warer	EPO; JPO;	
		a.		IBM_TDB	
18		. 13	((700/109.ccls. and (delay or hysterisis or	USPAT;	2004/10/13 22:58
			hysteresis) ) and wafer) and (model\$5 or	US-PGPUB;	
			simulat\$5 or emulat\$5)	EPO; JPO;	
		_		IBM_TDB	0004/10/10 00 00
19		0	(((700/109.ccls. and (delay or hysterisis or	USPAT; US-PGPUB;	2004/10/13 22:58
			hysteresis) ) and wafer) and (model\$5 or simulat\$5 or emulat\$5)) and (threshold with	EPO; JPO;	
			voltage)	IBM TDB	
20		50	700/110 ccls and (delay or hysterisis or	USPAT;	2004/10/13 22:58
			hysteresis)	US-PGPUB;	,,
				EPO; JPO;	
				IBM_TDB	
21		19		USPAT;	2004/10/13 22:58
1			hysteresis) ) and wafer	US-PGPUB;	
				EPO; JPO; IBM TDB	
22		11	((700/110.ccls. and (delay or hysterisis or	USPAT;	2004/10/13 22:58
22			hysteresis) ) and wafer) and (model\$5 or	US-PGPUB;	2001/10/13 22.30
			simulat\$5 or emulat\$5)	EPO; JPO;	
				IBM_TDB	
23		2	1	USPAT;	2004/10/13 22:58
			hysteresis) ) and wafer) and (model\$5 or	US-PGPUB;	
			simulat\$5 or emulat\$5)) and (threshold with	EPO; JPO;	
		_	voltage) ((BERTSCH-JOHN-E BERTSCH-JOHN-EDWARD	IBM_TDB	2004/10/01 10:29
-		- 2	((BERTSCH-JOHN-E BERTSCH-JOHN-EDWARD   BERTSCH-JOHN COOPS-DANIEL-S FRIED-DAVID	USPAT; US-PGPUB;	2004/10/01 10:29
			FRIED-DAVID-M).in.) and (simulat\$5 or	EPO; JPO;	
			emulat\$5)	IBM TDB	
-		43	· ·	USPAT;	2004/09/30 12:08
			BERTSCH-JOHN COOPS-DANIEL-S FRIED-DAVID	US-PGPUB;	
1.			FRIED-DAVID-M).in.	EPO; JPO;	
,		_		IBM_TDB	
1 -		1	(model same hardware same correlation) and	USPAT;	2004/10/01 10:29
			MHC	US-PGPUB; EPO; JPO;	
				IBM TDB	
_		140	model same hardware same correlation	USPAT;	2004/10/01 10:30
			•	US-PGPUB;	,,
	, .			EPO; JPO;	
				IBM_TDB	
<del>-</del>		21	model with hardware with correlation	USPAT;	2004/10/01 10:38
				US-PGPUB; EPO; JPO;	
			*	IBM TDB	
_		11	model near3 hardware near3 correlation	USPAT;	2004/10/01 10:52
				US-PGPUB;	,,
				EPO; JPO;	
				IBM_TDB	
- ₹.		. 659	wafer with final with test	USPAT;	2004/10/01 10:52
				US-PGPUB;	
				EPO; JPO; IBM TDB	
_		223	(wafer with final with test) and (line with	USPAT;	2004/10/01 10:57
			test)	US-PGPUB;	111, 11, 11, 11
			·	EPO; JPO;	
				IBM_TDB	
-		91	(wafer with final with test) and (in with	USPAT;	2004/10/01 10:57
			line with test)	US-PGPUB;	
			,	EPO; JPO;	
_		1	(model same hardware same correlation) and	IBM_TDB USPAT;	2004/10/01 11-22
-			((wafer with final with test) and (in with	US-PGPUB;	2004/10/01 11:22
			line with test))	EPO; JPO;	'
				IBM TDB	
-		1581	(mos or cmos or mosfet) same (verify or	USPAT;	2004/10/01 11:23
			verification or verifying)	US-PGPUB;	
				EPO; JPO;	
1				IBM_TDB	

Page 2

	248	((mos or cmos or mosfet) same (verify or	USPAT;	2004/10/01 11:24
-	246	verification or verifying)) and wafer	US-PGPUB;	2004/10/01 11.24
		verification of verifying), and water	EPO; JPO;	
			IBM_TDB	
	8	(((mos or cmos or mosfet) same (verify or	USPAT;	2004/10/01 11:40
		verification or verifying)) and wafer) and	US-PGPUB;	2001, 20, 01 11.10
		(source adj current) and (drain adj current)	EPO; JPO;	
		(source adj currency and (drain adj currency	IBM TDB	
_	89	(((mos or cmos or mosfet) same (verify or	USPAT;	2004/10/01 11:26
	0,	verification or verifying)) and wafer) and	US-PGPUB;	
		(source with current) and (drain with	EPO; JPO;	
		current)	IBM TDB	
_	3	l	USPAT;	2004/10/01 11:27
		verification or verifying)) and wafer) and	US-PGPUB;	
		(source with current) and (drain with	EPO; JPO;	
		current)) and (AC or (alternating adj	IBM TDB	
		current))	_	
_	3	(((((mos or cmos or mosfet) same (verify or	USPAT;	2004/10/01 11:27
		verification or verifying)) and wafer) and	US-PGPUB;	
		(source with current) and (drain with	EPO; JPO;	
1		current)) and (AC or (alternating adj	IBM_TDB	'
		current))) and (DC or (direct adj current))		
-	15		USPAT;	2004/10/01 17:00
		verification or verifying)) and wafer) and	US-PGPUB;	
		(spice)	EPO; JPO;	
			IBM_TDB	0004/00/00
-	93	(spice and wafer and model and (test or	USPAT;	2004/10/01 17:01
	1	verif\$7) and parameter) and @ad<20010529	US-PGPUB;	
	1		EPO; JPO;	
***************************************	151	spice and wafer and model and (test or	<pre>IBM_TDB USPAT;</pre>	2004/10/12 13:44
-	151	verif\$7) and parameter	US-PGPUB;	2004/10/12 13:44
		Verify) and parameter	EPO; JPO;	
			IBM TDB	,
_	5	spice and wafer and model and (test or	USPAT;	2004/10/06 17:07
		verif\$7) and parameter and 702/\$3.ccls.	US-PGPUB;	
			EPO; JPO;	
			IBM TDB	
_	185	spice and (wafer and model) and (test or	USPAT;	2004/10/12 13:45
		verif\$7)	US-PGPUB;	
			EPO; JPO;	
			IBM_TDB	
-	39	1 ± ''	USPAT;	2004/10/12 13:45
		verif\$7))	US-PGPUB;	
			EPO; JPO;	
			IBM_TDB	0004/10/10 10 10
-	1	1 -1	USPAT;	2004/10/13 18:02
	1	verif\$7) and kerf	US-PGPUB;	,
		1 .	EPO; JPO;	
	23	kerf same oscillator	IBM_TDB USPAT;	2004/10/13 22:43
_	23	VELL Same OSCILLACOL	US-PGPUB;	2004/10/13 22:43
	1 .		EPO; JPO;	(4) (1) 4
			IBM TDB	W W 70
I	1	[	1	l